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APPLICANTS

Yoshiaki Kamigaki, Tokorozawa, JAPAN;
 Shinichi Minami, Kodaira, JAPAN;
 Kozo Katayama, Kokubunji, JAPAN;
 Masataka Kato, Koganei, JAPAN;

** CONTINUING DATA *****

This application is a CON of 10/377,785 03/04/2003 PAT 6,674,122
 which is a CON of 09/660,923 09/13/2000 PAT 6,531,735

** FOREIGN APPLICATIONS *****

JAPAN 2000-83246 03/21/2000
 JAPAN 11-263154 09/17/1999
 JAPAN 11-263155 09/17/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 68	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials			

ADDRESS

MATTINGLY, STANGER & MALUR, P.C.
 Suite 370
 1800 Diagonal Road
 Alexandria, VA 22314

TITLE

Semiconductor integrated circuit having two switch transistors formed between two diffusion-layer lines

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